

S/N 10/642,702

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Vivek Jaiswal et al.	Examiner:	Chirag R. Patel
Serial No.:	10/642,702	Group Art Unit:	2454
Filed:	August 18, 2003	Docket No.:	P16507
Title:	DYNAMIC LOAD DISTRIBUTION WITHIN A SESSION INITIATION PROTOCOL NETWORK		
Assignee:	Intel Corporation	Confirmation No.: 4365	

RESPONSE AFTER FINAL REJECTION

OK TO ENTER: /CP/

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

This is in response to the Final Office Action mailed June 1, 2009.¹ No amendments are being made to the subject application by the within Response. Accordingly, it is believed that Applicants are entitled to consideration and entry of the within Response as a matter of right. Reconsideration and allowance of the subject application, in light of the following Remarks, are respectfully requested.

¹ In the Examiner-Initiated Interview Summary mailed June 22, 2009, the Examiner has indicated that the finality of the Office Action mailed March 18, 2009 has been withdrawn and said Office Action has been replaced with the Final Office Action mailed June 1, 2009. The within Response is being filed before the expiration of the two-month date for response to the Final Office Action mailed June 1, 2009, which, as a result of the foregoing, is the only Office Action in the subject application to which response is required.